

NOAO

ENGINEERING CHANGE ORDER

BOARD NAME <u>TORRENT CCD ANALOG FRONT END</u>	ECO# TRNT-41	DATE <u>19/07/16</u>
BRD SERL# <u>40-41</u> REV _____	ART# _____	
PN# <u>TRNT-EL-04-1004</u> REV <u>C</u>	REV _____	
ASBLY# _____ REV _____	PCB# <u>TRNT-EL-04-2004</u>	REV <u>C1</u>
BOM# _____ REV _____	SCH# <u>TRNT-EL-04-2004</u>	REV <u>C1</u>
COGNIZANT ENGNR <u>Peter Moore & Braulio Cancino</u>	CHARGE# _____	

REASON FOR MODIFICATION:

Special Analog Front End for 0.9m Tek – 2k.

Optimize frequency response of the input filter and the time constant of the integrator of both boards.

DRAWINGS AFFECTED:	NEW REV
TRNT-EL-04-2004	C1

DESCRIPTION OF MODIFICATION:

1. Change: R137 = R138 = R125 = R122 = 221kΩ – Increased bandwidth by decreasing the input amplifier low pole frequency to reduce sample sag during the integration time.
2. Change: R246 = R221 = R199 = R175 = R250 = R225 = R203 = R180 = 3.32kΩ – Increased integrator time constant.