

NOAO

ENGINEERING CHANGE ORDER

BOARD NAME <u>TORRENT Mezzanine Board</u>	ECO# TRNT-027	DATE <u>10/26/11</u>
BRD SERL# <u>007 > ALL REV A</u> REV <u>A</u>	ART# <u>TRNT-EL-07-0003-007 ></u>	
PN# _____ REV _____	REV _____	
ASBLY# <u>TRNT-EL-04-0001</u> REV <u>A1</u>	PCB# <u>TRNT-EL-04-1003</u>	REV <u>A</u>
BOM# <u>TRNT-EL-04-4003</u> REV <u>A3</u>	SCH# <u>TRNT-EL-04-2003</u>	REV <u>A1</u>
COGNIZANT ENGR <u>Peter Moore</u>	CHARGE# _____	

REASON FOR MODIFICATION:

Allow high voltage power supply control under low temperature conditions. Under low temperature conditions the LT4356 devices show excessive leakage current on pin 1 which drives the TMR pin to a level above 1.25v where turn on of the high voltage supply is inhibited. The values measured are 5.5v on pin 1 with leakage measured at approx. 17ua. Under these conditions pin 9 pullup current is also weakened considerably.

DRAWINGS AFFECTED:	NEW REV
TRNT-EL-04-2003	A2
TRNT-EL-04-4003	A3

DESCRIPTION OF MODIFICATION:

1. Add a 30.0K Ohm resistor in parallel with C27 and C28, R101, R136 respectively
2. Add a 100K Ohm resistor between pins U3:8 to U3:9 and U6:8 TO U6:9, R137 and R138 respectively