NOAO

ENGINEERING CHANGE ORDER

9BOARD NAME	Mezzanine Board			ECO#	**TRNT-010**	DATE	03/29/10
BRD SERL#	= > 007	REV		ART#			
PN#	TRNT-EL-04-1003	REV	A			REV	
ASBLY#	TRNT-EL-04-0003	REV	A	PCB#		REV	
BOM#	TRNT-EL-04-4003	REV	A1	SCH#	TRNT-EL-04-2003	REV	A
COGNIZANT ENGNR	R. George / D. Sawyer			APPROVD	Mark Hunten		

Digitally signed by Mark Hunten
DN: cn=Mark Hunten, o=NOAO, ou=MONSOON, email=mhunten@noao.edu, c=US
Date: 2010.06.25 09:23:50 -07'00'

REASON FOR MODIFICATION:

Adjust +/-18V current limit
Adjust +/-18V timer circuit
Adjust +18V gate delay time
Adjust +30V date delay time

DRAWINGS AFFECTED:	NEW REV
TRNT-EL-04-4003	A2
TRNT-EL-04-2003	A1

DESCRIPTION OF MODIFICATION:

+/-18V circuits, AFE1 and AFE2,

R42 and R34 change from 1.5Kohm to 9.53Kohm
C14 and C9 change from 0.1uF to .47uF
C15 and C10 change from “open” to .047uF

+/-30V circuits, AFE1 and AFE2,

C19 and C20 change from .068uF to “open”